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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/910,096	07/19/2001	David Chaiken	AGLE0028	3279

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GLENN PATENT GROUP
3475 EDISON WAY, SUITE L
MENLO PARK, CA 94025

EXAMINER

MARTINEZ, DAVID E

ART UNIT	PAPER NUMBER
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2182

DATE MAILED: 02/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/910,096

Applicant(s)

CHAIKEN ET AL.

Examiner

David E Martinez

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 July 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 11 September 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 9/11/01.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 6 and 16 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

With regards to claims 6 and 16, they call for "a plurality of servers, each of said servers defined by one or more systems-on-a-chip; and a plurality of servers connected by said standard data communications network to define a server farm". It is not clear if the first plurality of servers is the same as the second plurality of servers or if they are separate groups of servers distinct from each other. Due to the vagueness and a lack of clear definiteness used in the claims, the claims have been treated on their merits as best understood by the examiner.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1- 21 are rejected under 35 U.S.C. 102(e) as being anticipated by US Patent No. 6,002,851 to Basavaiah et al (Basavaiah).

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With regards to claims 1 and 11, Basavaiah teaches a network architecture [fig 1], comprising:

a server farm [fig 1] comprised of a plurality of systems-on-a-chip [fig 1 elements 112a, 112b, 112n];

each system-on-a-chip [fig 1 elements 112a, 112b, 112n] implementing one or more integrated standard or other network interfaces [fig 1 elements 117a, 117b, 117n]; and

an interconnect scheme [fig 1 element 114] for connecting said system-on-a-chip [fig 1 elements 112a, 112b, 112n] via said network interfaces [fig 1 elements 117a, 117b, 117n], wherein said interconnect scheme eliminates physical interfaces that are otherwise required to establish communications between servers in a server farm [column 12 lines 48-67].

With regards to claims 2 and 12, Basavaiah teaches the network architecture of Claim 1, said interconnect scheme comprising a point-to-point scheme [figs 1-5, column 12 lines 48-67].

With regards to claims 3 and 13, Basavaiah teaches the network architecture of Claim 1, said standard network interface comprising Ethernet [column 12 lines 48-67].

With regards to claims 4 and 14, Basavaiah teaches the network architecture of Claim 1, said interconnect scheme comprising a back plane comprising a plurality of connectors [column 12 lines 48-67].

With regards to claims 5, and 15, Basavaiah teaches the network architecture of Claim 1, said systems-on-a-chip being interconnected to define two or more PLEX arrays [column 12 lines 48-67].

With regards to claims 6 and 16 and 17, Basavaiah teaches a data communications network [fig 1], comprising:

a standard networking component [fig 1 elements 117a, 117b, 117n, and 114] for implementing a standard data communications network [column 12 lines 48-67];

a plurality of systems-on-a-chip [fig 1 elements 112a, 112b, 112n] that each comprise at least one processor [fig 1 elements 112a, 112b, 112n] and that each provide one or more standard network interfaces [fig 1 elements 117a, 117b, 117n];

a plurality of servers [fig 1 elements 112a, 112b, 112n], each of said servers defined by one or more systems-on-a-chip [fig 1 elements 112a, 112b, 112n]; and

a plurality of servers [fig 1 elements 112a, 112b, 112n] connected by said standard data communications network [fig 1 element 114] to define a server farm [fig 1]; and

an interconnect scheme [fig 1 element 114] for connecting said systems-on-a-chip [fig 1 elements 112a, 112b, 112n] via said network interfaces [fig 1 elements 117a, 117b, 117n], wherein said interconnect scheme eliminates physical interfaces that are otherwise required to establish communications between servers in said server farm [column 12 lines 48-67].

With regards to claims 7 and 18, Basavaiah teaches the network of Claim 6, said interconnect scheme comprising a point-to-point scheme [figs 1-5, column 12 lines 48-67].

With regards to claims 8 and 19, Basavaiah teaches the network of Claim 6, said standard network interface comprising Ethernet [column 12 lines 48-67].

With regards to claims 9 and 20, Basavaiah teaches the network architecture of Claim 1, said interconnect scheme comprising a back plane comprising a plurality of connectors [column 12 lines 48-67].

With regards to claims 10 and 21, Basavaiah teaches the network architecture of Claim 1, said systems-on-a-chip being interconnected to define two or more PLEX arrays [column 12 lines 48-67].

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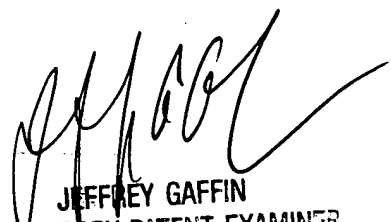
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David E Martinez whose telephone number is (571) 273-4152. The examiner can normally be reached on 8:30-5:00 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey A Gaffin can be reached on (571) 272-4146. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DEM


JEFFREY GAFFIN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100